

AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph on page 1, lines 8-17, as follows:

The present application is related to co-pending U.S. Patent Application Serial No. 09/589,663 (IBM Docket No. AUS990940US1) entitled "HYPERVISOR AS A SET OF SERVICES" filed June 8, 2000 and to U.S. Patent Application Serial No. 09/589,665 (IBM Docket No. AUS990941US1) entitled "DMA WINDOWING" filed June 8, 2000 and issued September 30, 2003 as U.S. Patent 6,629,162. The content of the above-mentioned commonly assigned, co-pending U.S. Patent applications are hereby incorporated herein by reference for all purposes.

Please amend the paragraph on page 18, line 25, to page 19, line 10, as follows:

Below, in **Table 1**, is an exemplary table of function sets and their mandatory/optional status. The hcall-pft functions manipulate the page frame table for processor virtual address translation. The [[the]] hcall-tce function set manipulates the Direct Memory Access (DMA) facilities used by the IO devices as described in U.S. Patent Application Serial Number 09/589,665 (IBM Docket No. AUS990941US1) entitled "DMA Windowing" filed on June 8, 2000, and issued September 30, 2003 as U.S. Patent 6,629,162. The hcall sprg0, hcall-dabr and hcall-asr functions manipulate internal processor registers which may themselves be optional. The hcall-debug[, &]] and hcall-perf function sets provide services needed by debuggers and performance monitor routines. The hcall-term function set provides the function described in the virtual terminal invention disclosure. Finally hcall-dump provides facilities to allow an OS to do a dump of the Hypervisor data areas for platform debug.